



2183

Attorney/Docket No. 26530.89
Customer No. 000027683

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:
Ricart et al.

Serial No. 10/636,118

Filed: 08/07/2003

For: Task Distribution

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§ Group Art Unit: 2183
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§ Examiner: Unknown
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INFORMATION DISCLOSURE STATEMENT

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In compliance with the duty of disclosure under 37 CFR §1.56, and in accordance with the practice under 37 CFR §1.97 and §1.98, the Examiner's attention is directed to the documents listed on the enclosed modified Form PTO-1449. No inference should be made that the cited references are in fact material, are in fact prior art, or that no better art exists. The cited patents are listed in numerical order and are not in any order based on their pertinence.

Pursuant to 37 CFR §1.98(d), no copies of the cited references are enclosed since the references were provided in the parent application, U.S. Serial No. 09/354,429, now issued patent 6,647,408.

This Information Disclosure Statement is being filed within three months of the United States filing date or before the mailing date of a first Office Action on the merits. No certification or fee is required (37 CFR §1.97(b)).

It is requested that the Examiner fully consider the cited references and that they be cited on the front of any patent issuing from this application.

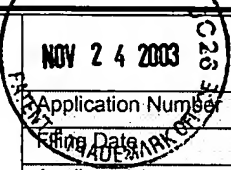
An early action on the merits is respectfully requested.

Respectfully submitted,

David L. McCombs
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Date: 19 Nov 2003
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<p align="center">Certificate of Mailing</p> <p>I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner For Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 11-20-03.</p> <p align="center"> Gayle Conner</p>

In place of PTO-1449 Form	U. S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		Complete if Known
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Application Number	10/636,118
		Filing Date	08/07/2003
		Applicant(s)	Ricart et al.
		Art Unit	2183
		Examiner Name	Unknown
		Attorney Docket Number	26530.89
SHEET	1	OF	1

U. S. PATENT DOCUMENTS				
Examiner's Initials	Cite No.	Document Number	Publication Date <small>MM-DD-YYYY</small>	Name of Patentee or Applicant of Cited Document
	AA	4,459,664	07/10/1984	Pottier et al.
	AB	4,590,555	05/20/1986	Bourrez
	AC	5,103,393	04/07/1992	Harris et al.
	AD	5,146,540	09/08/1992	Natarajan
	AE	5,197,140	03/23/1993	Balmer
	AF	5,680,634	10/21/1997	Estes

FOREIGN PATENT DOCUMENTS					
Examiner's Initials	Cite No.	Foreign Patent Document <small>(Country Code - Number - Kind)</small>	Publication Date <small>MM-DD-YYYY</small>	Patentee or Applicant of Cited Document	Translation <small>Y/N</small>

OTHER PRIOR ART		
Examiner's Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item, date, page(s), volume, issue number(s), publisher, city/country where published
	AG	Munkres, <u>Topology: A First Course</u> , Prentice-Hall, Inc., Englewood Cliffs, NJ, pp.98, 126, 279; 1975
	AH	Ricart, "Efficient Synchronization Algorithms for Distributed Systems", University Microfilms International, Ann Arbor, MI, 1980
	AI	Anderson et al., "Performance Implications of Thread Management Alternatives for Shared-Memory Multiprocessor", IEEE Transactions on Computers, Volume 38, No. 12, December 1989
	AJ	Ray Tracing News, "Light Makes Right", Volume 2, Number 8, pp. 1-5; 1989
	AK	Deo et al., "Processor Allocation in Parallel Battlefield Simulation", Proceedings of the 1992 Winter Simulation Conference, 1992
	AL	Silberschatz et al., <u>Operating System Concepts</u> , fourth edition, Addison-Wesley Publishing Company, pp. 659-689; 1994
	AM	Liu et al., "Non-contiguous Processor Allocation Algorithms for Distributed Memory Multicomputers", IEEE 1994
	AN	McCann et al., "Processor Allocation Policies for Message-Passing Parallel Computers", SIGMETRICS 94-5/94, Santa Clara, CA 1994
	AO	Thekkath et al., "Impact of Sharing-Based Thread Placement on Multithreaded Architecture", IEEE 1994
	AP	Tanenbaum, <u>Distributed Operating Systems</u> , Prentice-Hall, Inc., Englewood Cliffs, NJ, pp. 169-213, 234-241; 1995
	AQ	Zarantonello, "Hilbert Space", pp. 1-2, 1997

Examiner Signature	Date Considered
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.